

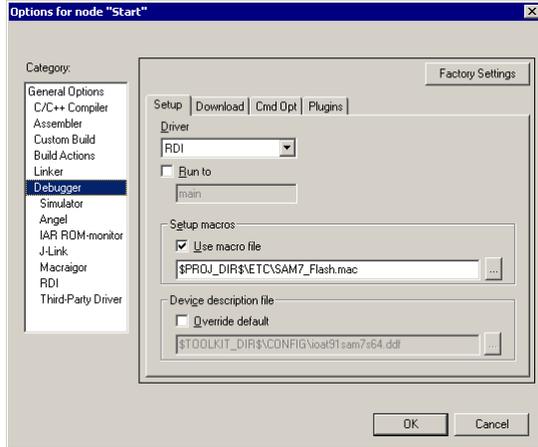
Settings for AT91 SAM7S64 and IAR

Double click the workspace file.

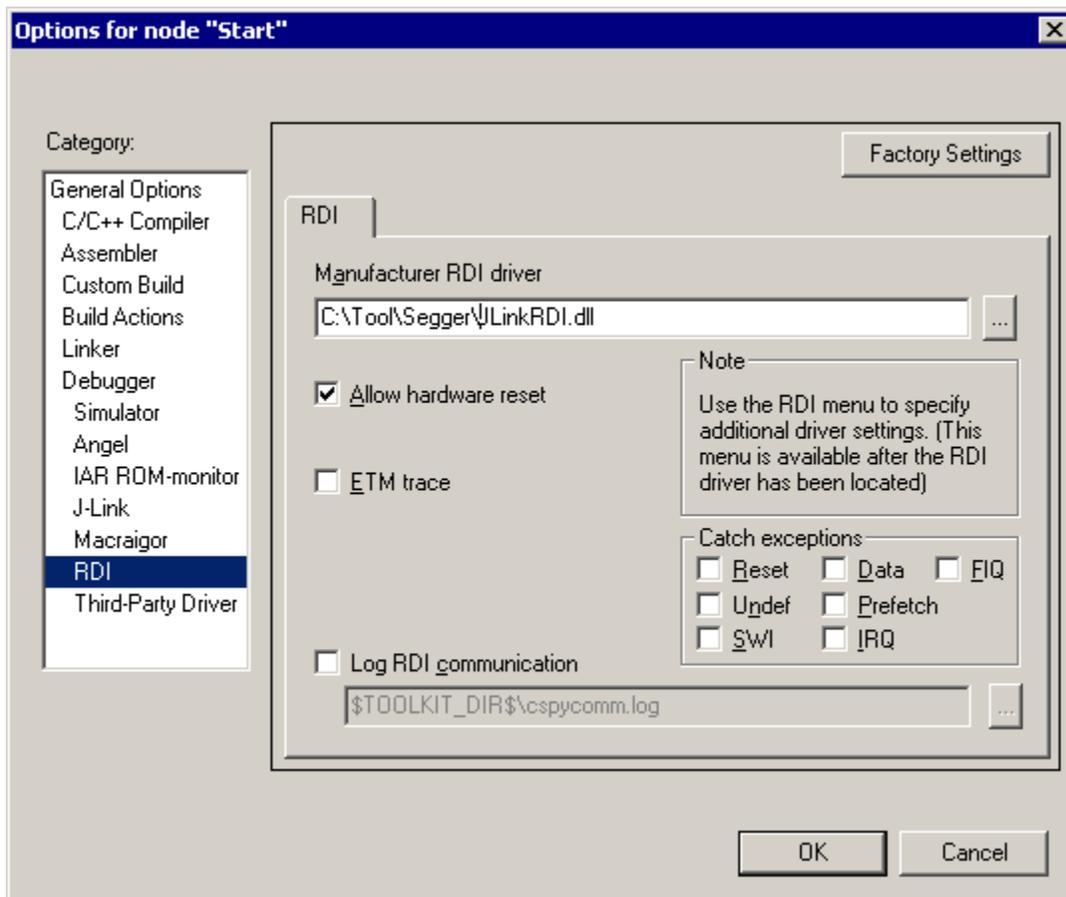
Select "FlashDebug" Config.

Make sure settings are as shown below:

IAR Settings



Browse to your RDI DLL:



J-Link RDI Setting

Select Menu point RDI|Configure and make sure the settings are as shown below.

The image displays four sequential screenshots of the J-Link RDI Configuration dialog box, showing the settings for different tabs: General, JTAG, Flash, and Breakpoints.

General Tab: Shows the J-Link RDI hardware and a description of the software. The 'Connection to J-Link' is set to USB. There is a 'Log to file' checkbox and a 'Location of config file' field. Buttons for 'About', 'License', and 'Reset Config' are visible.

JTAG Tab: Shows 'JTAG speed' set to 'Auto selection' and 'JTAG scan chain with multiple devices' with 'Position' and 'IR len' set to 0. A 'Verify JTAG config' button is present.

Flash Tab: Shows 'Enable flash programming' checked. 'Cache flash contents' is checked. 'Flash memory' is set to 'Atmel AT91SAM7S64' and 'Flash @' is '0x0'. 'Clock speed' is '48000000 Hz'. 'Allow flash download' is unchecked, and 'Show info window during download' is checked.

Breakpoints Tab: Shows 'Use software breakpoints' checked. 'Use flash breakpoints' is checked. 'Show info window during program' is unchecked.

Final Tab (likely CPU or another): Shows 'Allow instruction set simulation' checked.